

Docket No.: 01-490

PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
Michael Eneboe, et al.

: By the Examiner:

Serial No.:

: Express Mail No.: EV 013 245 418 US

Filed: October 30, 2001

: Group Art Unit:

Title: SYSTEM AND METHOD FOR OPTIMIZING AN INTEGRATED CIRCUIT DESIGN

Assistant Commissioner for Patents  
Washington, D.C. 20231



CERTIFICATION UNDER 37 C.F.R. §1.10

I hereby certify that this Application Transmittal and the documents referred to as enclosed therein are being deposited with the United States Postal Service on this date, October 30, 2001 in an envelope as "Express Mail Post Office to Addressee", Mailing Label N° EV 013 245 418 US addressed to: Box Patent Application, Assistant Commissioner for Patents, US PTO, Washington, D.C. 20231.

Penny L. Flint  
Penny L. Flint

DATED: October 30, 2001

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicant submits herewith patents, publications or other information of which he is aware, which he believes may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR 1.98(a)(1).

A copy of each of these items on PTO-1449 is supplied herewith.

DATED: October 30, 2001.

Respectfully submitted,  
Michael Eneboe, et al.,

By:



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